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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Robert A. Aekins, et al. )  
Serial No.: 09/982,073 ) Examiner:  
Filed: October 17, 2001 ) Group Art Unit: 2817  
For: DUAL REACTANCE LOW NOISE MODULAR CONNECTOR INSERT ) Confirmation No. 8240

Commissioner for Patents  
Washington, D.C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. § 1.56, the reference listed on the attached form PTO-1449 is being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application.

The Supplemental Information Disclosure Statement submitted herewith is being filed, to the best of applicants' knowledge, before the mailing date of a first Office Action on the merits.

The filing of this Supplemental Information Disclosure Statement shall not be construed to be a representation that a search has been conducted, nor shall it be construed as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

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It is respectfully requested that the Examiner return a copy of the attached form PTO-1449 with initials or other appropriate marks indicating consideration of the cited materials.

Respectfully submitted,



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Date: January 7, 2003

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I hereby certify that the enclosed Supplemental Information Disclosure Statement along with a PTO-1449 Form and the cited references are being deposited with the United States Postal Service as first class mail, postage prepaid, addressed to the Commissioner for Patents, Washington, D.C. 20231, on January 7, 2003.

Dated: January 7, 2003



Joan Simmons

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